

**REMARKS**

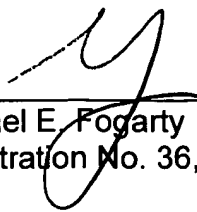
The foregoing amendment corrects an inadvertent error contained in the application as originally filed, which was the inclusion of two claims numbered 28. The second claim 28 has been deleted and resubmitted as new claim 37. No new matter has been added.

Respectfully submitted,

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Date: 4/23/02

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WDC99 573104-1.057457.0012

**VERSION WITH MARKINGS TO SHOW CHANGES MADE**

**IN THE CLAIMS:**

On page 40, the second claim 28 has been canceled.

New claim 37 has been added as follows:

-- 37. (New) The method of recovering a clock signal according to claim 26, wherein said pulse width of the incoming data signal is extended by  $\frac{2}{8}$  of a clock cycle when said data sample received from said sampling unit is present for only  $\frac{5}{8}$  of the entire clock cycle.--